Virtual eXecuter – The VX platform

A virtual machine for virtually any microcontroller

Table of contents

[Preface 5](#_Toc190585639)

[Revision track 5](#_Toc190585640)

[Data types 6](#_Toc190585641)

[Memory sections 6](#_Toc190585642)

[The status register 6](#_Toc190585643)

[Program execution 6](#_Toc190585644)

[Instruction set 7](#_Toc190585645)

[How to read the instruction set 7](#_Toc190585646)

[Arithmetic 7](#_Toc190585647)

[Add 7](#_Toc190585648)

[Sub 8](#_Toc190585649)

[Mul 8](#_Toc190585650)

[Inc 9](#_Toc190585651)

[Dec 9](#_Toc190585652)

[Transfer 9](#_Toc190585653)

[Load 9](#_Toc190585654)

[Copy 10](#_Toc190585655)

[Swap 10](#_Toc190585656)

[Car 10](#_Toc190585657)

[Logical 11](#_Toc190585658)

[And 11](#_Toc190585659)

[Or 11](#_Toc190585660)

[XOr 11](#_Toc190585661)

[Com 12](#_Toc190585662)

[Neg 12](#_Toc190585663)

[Shf 12](#_Toc190585664)

[Rot 13](#_Toc190585665)

[Stack 13](#_Toc190585666)

[Push 13](#_Toc190585667)

[PushC 13](#_Toc190585668)

[Pop 14](#_Toc190585669)

[Branches 14](#_Toc190585670)

[Jmp 14](#_Toc190585671)

[JmpA 14](#_Toc190585672)

[JmpZ 14](#_Toc190585673)

[JmpNZ 15](#_Toc190585674)

[JmpC 15](#_Toc190585675)

[JmpNC 15](#_Toc190585676)

[JmpO 16](#_Toc190585677)

[JmpNO 16](#_Toc190585678)

[JmpN 16](#_Toc190585679)

[JmpNN 17](#_Toc190585680)

[JmpS 17](#_Toc190585681)

[JmpNS 17](#_Toc190585682)

[Call 18](#_Toc190585683)

[CallC 18](#_Toc190585684)

[Ret 19](#_Toc190585685)

[IO 19](#_Toc190585686)

[ByO 19](#_Toc190585687)

[ByI 19](#_Toc190585688)

[BiO 19](#_Toc190585689)

[BiI 20](#_Toc190585690)

[Misc 20](#_Toc190585691)

[Wait 20](#_Toc190585692)

[Threads 20](#_Toc190585693)

[Spawn 20](#_Toc190585694)

[Kill 20](#_Toc190585695)

[Suspend 20](#_Toc190585696)

[Resume 20](#_Toc190585697)

[Sleep 20](#_Toc190585698)

[Building the VX core 21](#_Toc190585699)

[Compile time settings 21](#_Toc190585700)

[Threading model 21](#_Toc190585701)

[The VX tool chain 21](#_Toc190585702)

[The VX assembler 21](#_Toc190585703)

[Sections 21](#_Toc190585704)

[Labels 22](#_Toc190585705)

[Comments 22](#_Toc190585706)

[Command line options 22](#_Toc190585707)

[Assembling a program 22](#_Toc190585708)

[The File Store Image Creator 22](#_Toc190585709)

[VX files 22](#_Toc190585710)

[Executables 23](#_Toc190585711)

[Load info 23](#_Toc190585712)

[Binary data 23](#_Toc190585713)

[Assembler source files 23](#_Toc190585714)

[List files 23](#_Toc190585715)

[Map files 24](#_Toc190585716)

[Preprocessor files 24](#_Toc190585717)

[Terminal interface 24](#_Toc190585718)

[The file system 24](#_Toc190585719)

[What’s next 24](#_Toc190585720)

[Compressed executables 24](#_Toc190585721)

[Enable programs to be made up of more than one source file 24](#_Toc190585722)

[FAT16 support 24](#_Toc190585723)

[Instruction set summary 25](#_Toc190585724)

[Terminal command summary 26](#_Toc190585725)

# Preface

The VX virtual machine is designed for small embedded systems originally intended for 8 bit microcontrollers.  
When building the virtual machine (VM) core a user provided configuration file is used to configure the system. This configuration file specifies which type of program memory is available, how (if at all) errors must be presented and handled and all the specifics of the hardware.

The preliminary focus is to define and implement a working VM. Execution speed and core size is not the focus of the first version and so it’s not expected to out perform any existing controller.

The instruction set is designed to allow for a short leaning period and will therefore look fairly similar to that of a standard 8 bit microcontroller. Two exceptions from this are that the instructions are stack based and that threading is supported directly on the lowest level (threading is not supported yet!).

Inspiration for the VX has been taken from the AVR, the PIC, the 8051, the x86, and the ARM hardcore microcontrollers, and the MicroBlaze and PicoBlaze softcore microcontrollers. Java byte code has also contributed to the development.

# Revision track

|  |  |  |
| --- | --- | --- |
| 0.01 | Jan 30 2008 | Document started. Created from the first bits and pieces of scribbled down notes and ideas. This is written before any code has been implemented and so represents preliminary thoughts and I-think-this-is-how-it-should-bes. |
| 0.02 | Feb 12 2008 | First published version of this document. Instruction set seems to be complete... Threading has been left out for now. |

# Data types

The VX instruction set operates on the following data types

* Boolean. A single bit.
* Single. A single byte representing an integer number.
* Double. Two bytes representing an integer number.
* Quad. Four bytes representing an integer number.
* Float. Four bytes representing a floating point number in the IEEE 754 format.

# Memory sections

Programs in the VX machine have the following memory sections

* Code. Non volatile. Read only. This is the only section that can be executed.
* Const. Non volatile. Read only.
* Data. Volatile. Both readable and writeable.
* Store. Non volatile. Both readable and writeable but only through push/pop instructions.

# The status register

The status register holds a number of flags that reflects the results and status of the previous instructions.  
The status register is not directly readable or writeable but reflects and affects the results of instructions.

The following flags is are placed in the status register

* Zero (Z)
* Carry (C)
* Two’s complement overflow (O)
* Negative (N)
* Sign (S)

The interpretation of these flags depends on the instruction being executed and will therefore be described together with the individual instructions.

# Program execution

When a program is loaded four base pointers are initialized. Each pointer points to the start of each section of the program. At the same time four size variables are loaded with the size of each section. This way sections can be placed freely in memory by the loaded as the base pointers are added to all memory location values in the program. The size variables are used to detect out-of-range access attempts. If out-of-range access is attempted the program is terminated prior to access.

# Instruction set

Each instruction is defined by a unique number in the range 0 through 255. I.e. each instruction op code takes up exactly one byte in the program storage media. Not all op codes are currently in use. Executing an unused op code terminates the program.

Values passed to the instruction directly from program memory are called constants. Far from all instructions require constants as most use arguments pushed from the stack.  
In assembly language constants are placed right after the instruction mnemonic separated by a space.  
Constants are placed on the memory location(s) immediately following the op code – little endian style where appropriate.

As the VX instruction set is stack based all arguments for the instructions must be pushed onto the stack prior to executing the instruction.  
Each instruction requires a specific number of values being pushed onto the stack. This number is referred to as the instructions argument count.  
The argument with the lowest number is the first one to be pushed onto the stack.  
When the instruction has completed all arguments will have been popped from the stack and return values, if any, will have been pushed in place. As before, the first value pushed will be number one.

## How to read the instruction set

The instructions are grouped together depending on the nature of the action they perform.

The description of each instruction has the following fields

* **Name**. The name of the instruction. This is not necessarily the mnemonic for the instruction as options are added to the name to form the final mnemonic. For instructions with no variants the name and the mnemonic are identical.
* **Description**. A description of the instruction and how it works.
* **Variants**. The variants of the instruction. The number in brackets indicates the variants op code.
* **Argument count**. The number of arguments popped from the stack by the instruction and the number pushed by the instruction.
* **Flags**. The flags used and/or affected by the instruction.
* **Size**. The number of bytes used to represent the instruction and
* **Usage.** A demonstration of how to use the instruction. For instructions with several options only one variant may be demonstrated.

The assembler is case insensitive and the mnemonics are only written with mixed case letters to increase readability.

## Arithmetic

Add

Adds the first argument to the second.  
Accepts variables of type single, double, quad and floating point.

Carry may be ignored or included when performing the operation but will always be affected by it.

Variants AddS [], AddD [], AddQ [], AddF [], AddSC [], AddDC [], AddQC [], AddFC []

Arg. count 2

Flags Z, C, O, N, S

Size 1

Usage PushD 102 ; arg 1 (the double at memory location 102)  
PushCD 20000 ; arg 2 (a constant)  
AddD ; perform an addition  
PopD 102 ; place the result back at memory location 102

Sub

Subtract the second argument from the first.  
Accepts variables of type single, double, quad and floating point.

Carry may be ignored or included when performing the operation but will always be affected by it.

Variants SubS [], SubD [], SubQ [], SubF [], SubSC [], SubDC [], SubQC [], SubFC []

Arg. count 2

Flags Z, C, O, N, S

Size 1

Usage PushQ 100 ; arg 1 (the quad at memory location 100)  
PushQ 104 ; arg 2 (the quad at memory location 104)  
SubQ ; sub ‘em  
PopQ 108 ; put result at location 108

Mul

Multiplies the second argument from the first.  
Accepts variables of type single, double, quad and floating point. When multiplying integers the output will always be the double width of the input values.

Carry may be ignored or included when performing the operation but will always be affected by it.

Variants MulS [], MulD [], MulQ [], MulF [], MulSC [], MulDC [], MulQC [], MulFC []

Arg. count 2

Flags Z, C, O, N, S

Size 1

Usage PushQ 100 ; arg 1 (the quad at memory location 100)  
PushQ 104 ; arg 2 (the quad at memory location 104)  
MulQ ; Multiply them  
PopQ 108 ; put result at location 108  
PopQ 112 ; ...and 112

Inc

Increments the value by one.  
Accepts variables of type single, double, quad and floating point.

Variants IncS [], IncD [], IncQ [], IncF []

Arg. count 1

Flags Z, C, O, N, S

Size 1

Usage PushS 100 ; push the value (the single at memory location 100)  
IncS ; add one  
PopS 100 ; and pop it back

Dec

Decrements the value by one.  
Accepts variables of type single, double, quad and floating point.

Variants DecS [], DecD [], DecQ [], DecF []

Arg. count 1

Flags Z, C, O, N, S

Size 1

Usage PushQ 100 ; push the value (the quad at memory location 100)  
DecQ ; subtract one  
PopQ 100 ; and pop it back

## Transfer

Load

Loads the constant into the memory location.  
Subtract the second argument from the first.  
Accepts variables of type single, double, quad and floating point.

Variants LoadS [], LoadD [], LoadQ [], LoadF []

Arg. count 0

Flags Z, C, O, N, S

Size 4/5/6/7

Usage LoadD 12345 100 ; put the double 12345 at location 100

Copy

Copies the contents of the first memory location to the second location.  
Accepts variables of type single, double, quad and floating point.

Variants CopyS [], CopyD [], CopyQ [], CopyF []

Arg. count 2

Flags Z, C, O, N, S

Size 1

Usage PushQ 100 ; arg 1 (the quad at memory location 100)  
PushQ 104 ; arg 2 (the quad at memory location 104)  
CopyQ ; copy contents of location 100 to location 104

Swap

Swaps the contents of the two memory locations.   
Accepts variables of type single, double, quad and floating point.

Variants SwapS [], SwapD [], SwapQ [], SwapF []

Arg. count 2

Flags Z, C, O, N, S

Size 1

Usage PushQ 100 ; arg 1 (the quad at memory location 100)  
PushQ 104 ; arg 2 (the quad at memory location 104)  
SwapQ ; swap contents of location 100 and location 104

Car

Clears or sets the carry flag.

Variants CarS [], CarC []

Arg. count 0

Flags -

Size 1

Usage CarC ; clear the carry flag

## Logical

And

Perform a bitwise and of the two values.   
Accepts variables of type single, double and quad.

Variants AndS [], AndD [], AndQ []

Arg. count 2

Flags Z, O, N, S

Size 1

Usage PushS 100 ; arg 1 (the single at memory location 100)  
PushCS 0x80 ; arg 2 (the constant 0x80)  
AndS ; do the and  
PopS 100 ; and back with it

Or

Perform a bitwise or of the two values.   
Accepts variables of type single, double and quad.

Variants OrS [], OrD [], OrQ []

Arg. count 2

Flags Z, O, N, S

Size 1

Usage PushS 100 ; arg 1 (the single at memory location 100)  
PushCS 0x80 ; arg 2 (the constant 0x80)  
OrS ; or ‘em  
PopS 100 ; and back with it

XOr

Perform a bitwise exclusive or of the two values.   
Accepts variables of type single, double and quad.

Variants XOrS [], XOrD [], XOrQ []

Arg. count 2

Flags Z, O, N, S

Size 1

Usage PushS 100 ; arg 1 (the single at memory location 100)  
PushCS 0x80 ; arg 2 (the constant 0x80)  
XOrS ; do the xor thing  
PopS 100 ; and back with it

Com

Complements the value.  
Accepts variables of type single, double and quad.

Variants ComS [], ComD [], ComQ []

Arg. count 1

Flags Z, O, N, S

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
ComD ; complement it  
PopD 100 ; and put it back

Neg

Negates the value.  
Accepts variables of type single, double and quad.

Variants NegS [], NegD [], NegQ []

Arg. count 1

Flags Z, O, N, S

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
NegD ; negate the value  
PopD 100 ; and put it back

Shf

Shifts the value one bit location left or right.  
Accepts variables of type single, double and quad.  
The bit being shifted in is always a 0. The bit being shifted out is placed in the carry flag.

Variants ShfLS [], ShfLD [], ShfLQ [],ShfRS [], ShfRD [], ShfRQ []

Arg. count 1

Flags Z, C, O, N, S

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
ShfLD ; shift it left  
PopD 100 ; and put it back

Rot

Rotates the value one bit location left or right.  
Accepts variables of type single, double and quad.  
The carry flag is rotated in and the bit being rotated out is placed in the carry flag after the rotation.

Variants RotLS [], RotLD [], RotLQ [],RotRS [], RotRD [], RotRQ []

Arg. count 1

Flags Z, C, O, N, S

Size 1

Usage PushCS 0 ; arg 1 – IO pin 0  
ByI ; copy pin state to carry  
PushCS 0 ; clear the value  
RotLS ; rotate it in as the LSB of value

## Stack

Push

Push the contents of the memory location onto the stack.  
Accepts variables of type single, double and quad.

Variants PushS [], PushD [], PushQ [], PushF []

Arg. count 1

Flags -

Size 3

Usage PushD 100 ; arg 1 (the double at memory location 100)  
ShfLD ; shift it left  
PopD 100 ; and put it back

PushC

Push the constant onto the stack.  
Accepts variables of type single, double and quad.

Variants PushCS [], PushCD [], PushCQ [], PushCF []

Arg. count 1

Flags -

Size 2/3/5

Usage PushCD 12345 ; arg 1 (the double constant 12345)  
ShfLD ; shift it left  
PopD 100 ; and put it in memory

Pop

Pop the top most value from the stack in to memory at the specified memory location.  
Accepts variables of type single, double and quad.

Variants PopS [], PopD [], PopQ [], PopF []

Arg. count 1

Flags -

Size 3

Usage PushD 100 ; arg 1 (the double at memory location 100)  
ShfLD ; shift it left  
PopD 100 ; and put it back

## Branches

Jmp

Unconditional jump to the address (a double) popped from the stack.

Variants Jmp []

Arg. count 1

Flags -

Size 1

Usage PushCD 82 ; arg 1 (the double constant 82)  
Jmp ; jump to address 82

JmpA

Unconditional absolute jump to the address specified by the constant.

Variants JmpA []

Arg. count 1

Flags -

Size 3

Usage JmpA 100 ; jump to address 100

JmpZ

Jump to the address (a double) popped from the stack if the zero flag is set.

Variants JmpZ []

Arg. count 1

Flags Z

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpZ ; jump to address 120 if the zero flag is set

JmpNZ

Jump to the address (a double) popped from the stack if the zero flag is cleared.

Variants JmpNZ []

Arg. count 1

Flags Z

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpNZ ; jump to address 120 if the zero flag is cleared

JmpC

Jump to the address (a double) popped from the stack if the carry flag is set.

Variants JmpC []

Arg. count 1

Flags C

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpC ; jump to address 120 if the carry flag is set

JmpNC

Jump to the address (a double) popped from the stack if the carry flag is cleared.

Variants JmpNC []

Arg. count 1

Flags C

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpNC ; jump to address 120 if the carry flag is cleared

JmpO

Jump to the address (a double) popped from the stack if the overflow flag is set.

Variants JmpO []

Arg. count 1

Flags O

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpO ; jump to address 120 if the overflow flag is set

JmpNO

Jump to the address (a double) popped from the stack if the zero flag is cleared.

Variants JmpNO []

Arg. count 1

Flags O

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpNO ; jump to address 120 if the overflow flag is cleared

JmpN

Jump to the address (a double) popped from the stack if the negative flag is set.

Variants JmpN []

Arg. count 1

Flags N

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpN ; jump to address 120 if the negative flag is set

JmpNN

Jump to the address (a double) popped from the stack if the negative flag is cleared.

Variants JmpNN []

Arg. count 1

Flags N

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpNN ; jump to address 120 if the negative flag is cleared

JmpS

Jump to the address (a double) popped from the stack if the sign flag is set.

Variants JmpS []

Arg. count 1

Flags S

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpS ; jump to address 120 if the sign flag is set

JmpNS

Jump to the address (a double) popped from the stack if the sign flag is cleared.

Variants JmpNS []

Arg. count 1

Flags S

Size 1

Usage PushD 100 ; arg 1 (the double at memory location 100)  
Dec  
PopD 100  
PushCD 120  
JmpNS ; jump to address 120 if the sign flag is cleared

Call

Calls the function at the location pushed to the stack. The address of the instruction immediately after the call is pushed to the stack.

Variants Call []

Arg. count 1

Flags -

Size 1

Usage PushD 100 ; arg 1 (the double at address 100)  
Call ; call function (effectively an indirect call)  
...  
PushQ 10 ; origin 100  
Dec  
PopQ 10  
Ret ; and return

CallC

Calls the function at the location indicated by the constant. The address of the instruction immediately after the call is pushed to the stack.

Variants CallC []

Arg. count 0

Flags -

Size 3

Usage PushD 100 ; argument for the function being called  
CallC 148 ; call function (direct call)  
...  
PushQ 10 ; origin 100  
Dec  
PopQ 10  
Ret ; and return

Ret

Pops the return address from the stack and program execution continues from that address.  
This is not an actual instruction but simply an alias for the Jmp instruction to ease transition to this instruction set from a standard microcontroller instruction set.

## IO

ByO

Transfers a value to an output port.

Variants ByO []

Arg. count 2

Flags -

Size 1

Usage PushS 100 ; the value to put on the port  
PushS 110 ; the port number  
ByO ; output it

ByI

Reads the value of an input port.

Variants ByI []

Arg. count 1

Flags -

Size 1

Usage PushS 110 ; the port number  
ByI ; read it

BiO

Transfers the value of the carry flag to a single port bit.

Variants BiO []

Arg. count 1

Flags -

Size 1

Usage PushCS 10 ; the bit number  
BiO ; output carry to bit

BiI

Transfers the value of a single port bit to the carry flag.

Variants BiI []

Arg. count 1

Flags -

Size 1

Usage PushCS 10 ; the bit number  
BiI ; read bit value to the carry flag

## Misc

Wait

Waits the specified number of milliseconds (a double) before proceeding to the next instruction.

Variants Wait []

Arg. count 1

Flags -

Size 1

Usage PushCD 10 ; the number of milliseconds  
Wait ; wait

## Threads

**THREADING IS NOT SUPPORTED YET!**

Spawn

Spawns a new thread and starts it.

Kill

Kills a thread.

Suspend

Suspends an existing thread.

Resume

Resumes a suspended or sleeping thread.

Sleep

Puts a task to sleep. If the task is already sleeping the longer of the two periods are used.

# Building the VX core

## Compile time settings

A number of settings can be adjusted before the VX VM firmware is compiled and programmed into the target controller. Using compile time configuration of the core firmware any program storage media can be used (internal EEPROM or flash, external flash card, USB port using a PC as virtual media to name a few).

The operating frequency of the processor must be configured allowing the firmware to adapt to any system speed. This affects the internal timers, the wait instruction, communication baud rates etc.

# Threading model

**THREADING IS NOT SUPPORTED YET!**

Loading a program creates a main process. This process can create threads. These must be given an appropriate amount of stack space (taken from the processes stack space).

Threads have the same priority as the process they exist in. Single process environments inherently have no priorities.

To start a new thread the following arguments are required

Start address

Stack space

# The VX tool chain

The VX platform is supported by a dedicated tool chain. All programs are made specifically for the VX VM.

The tools contained in the tool chain is

* **VXA.** The VX assembler. Generates .vxx executable files from .asm source files.
* **FSIC.** File Store Image Creator. Converts directories on a PC to image files (.vfi) for the file system used on the VX platform.

## The VX assembler

The VX assembler (VXA) is a command line tool capable of generating VX executables.

Only one instance of section, label or mnemonic can appear in a line.

### Sections

The assembler accepts four sections

* Code
* Const
* Stack
* Data

### Labels

Labels bind a human readable textual name to a memory address.

Labels can be used in all sections.

### Comments

Comments in the assembler source file is allowed by the assembler by removing any instances of ; (semicolon) and the rest of the line on which they were found.

### Command line options

Options are passed to the assembler when executing it.

The following lists the options available

* **Source file.** –s. The source file to assemble. If this option is omitted the default help screen will be shown.
* **Target core version.** –v. The VX core version that the program is intended for. If this option is omitted the generated executable will be made for the newest version supported by the assembler.
* **Generate list file.** –l. If this option is used a list file is generated displaying details on the generated program.
* **Generate preprocessor file.** –p. Generate a file containing the output from the preprocessor. The output has the extension .pre.
* **Generate map file.** –m. Generate map file when processing the .asm file.

### Assembling a program

To assemble a source file using the default options simply call the assembler with the –s source file option as shown below:

vxa –s hello.asm

This will, if the source file is valid, produce a file named hello.vxx.

## The File Store Image Creator

The internal microcontroller EEPROM is used for a tiny file system. Due to the extremely small size a read-only file system is used. Since files then can not be downloaded or created individually a binary image must be build on a PC and then downloaded. The files can then be read and loaded by the system.

# VX files

The VX system operates with a number of files. These are all described below. Some might be mostly self explanatory and so there description will be short.

## Executables

Virtual eXecuter program files with the extension .vxx (Virtual eXecuter Executable) contain the assembled program code, constants and any auxiliary program information that may be needed to load the program.

The file is divided into two sections

* Load info
* Binary data

### Load info

This section contains a number of fields that are used by the program loader to determine how to load the program and if it is at all possible to load and run it.

* **VX file type tag.** A valid VX executable file starts with a tag made up of the five ASCII letters “VXEXE”. The file extension is only to make it more human-friendly.  
  This is the only field in the load section that is human readable.
* **VX core version.** This is the core version that the file was generated for. This is a single byte.  
   If the version matches the actual core version that program loading will proceed. If the version does not match it is up to the core to decide whether it is able to execute the program or not. Backward compatibility is i.e. NOT guaranteed!
* **Code size.** This is the size of the program code that will be executed. This is a two byte integer.
* **Constant size.** This is the size of the constant section. This is a two byte integer.
* **Data size.** This is the size of the data section. This is a two byte integer.
* **Stack size.** This is the size of the stack section. This is a two byte integer.

The load info section takes up exactly 14 bytes.

If the tag is not correctly identified or if the executable version is not supported or if the total memory requirement exceeds the amount of free RAM the program is not loaded and an error message will be produced on the default communication channel.

### Binary data

The binary data section contains an image of what must be copied to memory i.e. the code and const sections. This section will be copied directly to the memory.

## Assembler source files

These are the source code of programs.

Assembler source files have the extension .asm.

## List files

A list file may be generated when assembling a source file to inspect the generated binary code.

List files have the extension .lst.

## Map files

A map file may be generated when assembling a source file to inspect the mapping of labels in memory.

Map files have the extension .map.

## Preprocessor files

The output of the preprocessor (first step in the assembling processor) may be output to a file.

Preprocessor files have the extension .pre.

# Terminal interface

The main interface to a system running the VX VM is through the terminal interface. This terminal provides a simple interface like that of DOS or similar. It enables a user to list and execute files and to upload new disc images. As only the read-only version of the file system is implemented modifications or upload of individual files is not possible.

## The file system

VX at first only support the VX File Store file system. This simple but ultra compact file system is read-only which in turn requires a disc image to be prepared on a PC using the VX File Store Image Creator (FSIC) application and then downloaded to the storage media. This download procedure can be done either trough a dedicated microcontroller programming tool or through the terminal interface.

Discs are mapped to single letters (again think DOS). The letter ‘A’ is reserved for internal microcontroller EEPROM (even if no EEPROM is present). Additional drives will be assigned letters in a yet unspecified manner but will depend on which disc interfaces is installed in the core.

# What’s next

## Compressed executables

In the future compressed executables may be implemented. These should have the VX file type tag “VXCEX” and will be decompressed when loaded. The three size values in the load info section will represent the in-memory size requirements.

## Enable programs to be made up of more than one source file

Currently the assembler only accepts one source file. This is obviously a short coming and should be extended.

It would be desirable if the –s source file option could be included in the assembler files. This would enable assembler files to automatically include the source files they them self require.

## FAT16 support

Support for the FAT16 file system must be added ASAP to enable SD memory cards written by a PC to be used as discs in a VX system.

# Instruction set summary

[Add](#_Toc190543025)

[Sub](#_Toc190543026)

[Mul](#_Toc190543027)

[Inc](#_Toc190543028)

[Dec](#_Toc190543029)

[Load](#_Toc190543030)

[Copy](#_Toc190543031)

[Swap](#_Toc190543032)

[Car](#_Toc190543033)

[And](#_Toc190543034)

[Or](#_Toc190543035)

[XOr](#_Toc190543036)

[Com](#_Toc190543037)

[Neg](#_Toc190543038)

[Shf](#_Toc190543039)

[Rot](#_Toc190543040)

[Push](#_Toc190543041)

[PushC](#_Toc190543042)

[Pop](#_Toc190543043)

[Jmp](#_Toc190543044)

[JmpA](#_Toc190543045)

[JmpZ](#_Toc190543046)

[JmpNZ](#_Toc190543047)

[JmpC](#_Toc190543048)

[JmpNC](#_Toc190543049)

[JmpO](#_Toc190543050)

[JmpNO](#_Toc190543051)

[JmpN](#_Toc190543052)

[JmpNN](#_Toc190543053)

[JmpS](#_Toc190543054)

[JmpNS](#_Toc190543055)

[Call](#_Toc190543056)

[CallC](#_Toc190543057)

[Ret](#_Toc190543058)

[ByO](#_Toc190543059)

[ByI](#_Toc190543060)

[BiO](#_Toc190543061)

[BiI](#_Toc190543062)

[Wait](#_Toc190543063)

[Spawn](#_Toc190543064)

[Kill](#_Toc190543065)

[Suspend](#_Toc190543066)

[Resume](#_Toc190543067)

[Sleep](#_Toc190543068)

# Terminal command summary

|  |  |
| --- | --- |
| cd | Change directory. Changes to the specified path or emits an error if path is unavailable.  As in DOS ‘..’ denotes the parent directory. |
| <disc>: | Changes to the disc with the letter specified or emits an error if disc is not available. |
| list | Lists files and directories in current directory. |
| print <file> | Prints the contents of the file specified or an error message if the file was unavailable. |
| <file> | Loads and executes the specified if the fail is available and if the file is a valid VX executable file. Otherwise an error message is printed. |
| load <disc> <size> | Loads a disc image to the disc specified through the terminal interface. The terminal interface channel is assumed to be reliable and so the image must be sent “as is” with no encoding. The size field is used to determine when the entire image has been transferred and to verify that the image fits on the disc.  To abort transfer after the command has been executed (but before the actual file transfer has commenced) press the ESC key. |